Silicon Phototransistor ST-3550B



Features

Fast response time

High photo sensitivity

Pb free

This product itself will remain within RoHS compliant version.

Application

Mouse

Optoelectronic Switch

Photo Interrupter

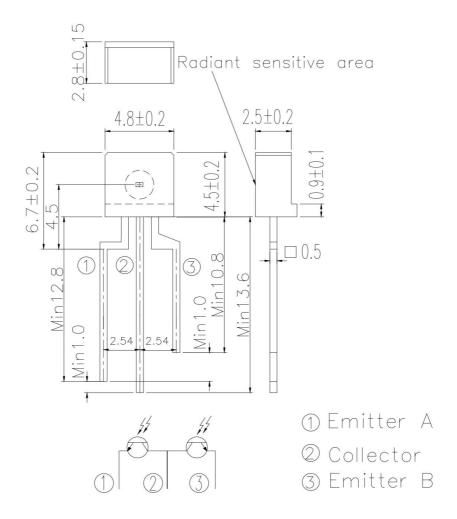
Description

ST3550B is a high speed and high sensitive dual phototransistor molded in a black plastic package with plat side view. The device is spectrally matched with IR emitters.





PACKAGE DIMENSIONS



NOTES:

- 1. All dimensions are in millimeters (inches).
- 2. Tolerance is ±0.25mm(.010") unless otherwise noted.
- 3. Lead spacing is measured where the leads emerge from the package.



ABSOLUTE MAXIMUM RATINGS AT TA =25°C

Parameter	Symbol	Rating	Units
Collector-Emitter Voltage	$ m V_{CEO}$	30	V
Emitter-Collector-Voltage	V_{ECO}	5	V
Collector Current	I_{C}	20	mA
Operating Temperature	Topr	-25 ~ +85°C	$^{\circ}\!\mathbb{C}$
Storage Temperature	Tstg	-40 ∼ +85°C	$^{\circ}\!\mathbb{C}$
Lead Soldering Temperature(*1)	Tsol	260	$^{\circ}\!\mathbb{C}$
Power Dissipation at (or below) 25°C Free Air Temperature	P_{D}	75	mW

Notes: *1 Soldering time≦5 seconds



ELECTRICAL OPTICAL CHARACTERISTICS AT TA=25°C

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Collector – Emitter Breakdown Voltage	$\mathrm{BV}_{\mathrm{CEO}}$	$I_{C}=100 \mu A$ $Ee=0 \text{mW/cm}^2$	30			V
Emitter-Collector Breakdown Voltage	$\mathrm{BV}_{\mathrm{ECO}}$	$I_E=100 \mu A$ $Ee=0 \text{mW/cm}^2$	5			V
Collector-Emitter Saturation Voltage	V _{CE(sat)}	I _C =2mA Ee=1mW/cm ²			0.4	V
Rise Time	t _r	V_{CE} =5 V I_{C} =1 mA		15		μS
Fall Time	t_{f}	$R_L = 1000 \Omega$		15		
Collector Dark Current	I_{CEO}	Ee=0mW/cm ² V _{CE} =20V			100	nA
On State Collector Current	I _{C(on)}	V_{CE} =5V, Ee=0.555mW/cm ²	129		1085	μ A
Wavelength of Peak Sensitivity	λр			940		nm
Rang of Spectral Bandwidth	λ 0.5		760		1100	nm



Typical Electro-Optical Characteristics Curves

Fig.1 Power Dissipation vs.

Ambient Temperature

100 80 80 80 40 40 20 -25 0 25 50 75 85 100 Ambient Temperature Ta (°C)

Fig.2 Spectral Sensitivity

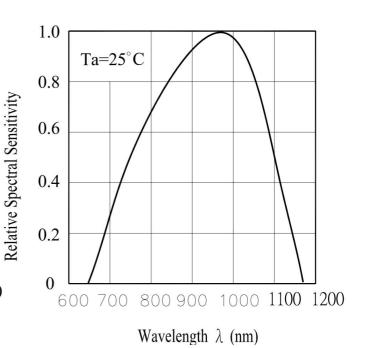


Fig.3 Dark Current vs. Ambient Temperature

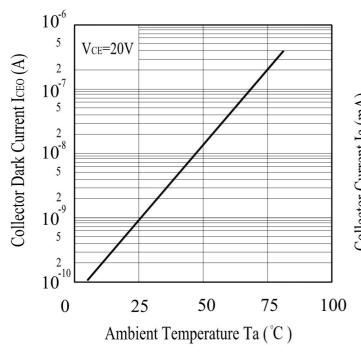
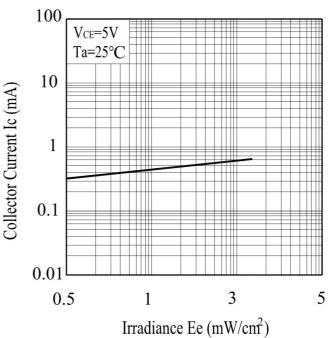


Fig. 4 Reverse Light Current vs. Ee





Typical Electro-Optical Characteristics Curves

Fig.5 Terminal Capacitance vs.

Reverse Voltage

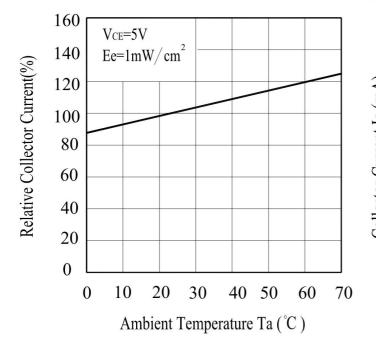
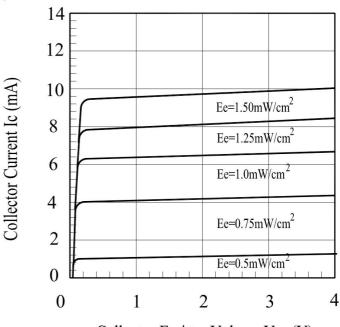


Fig.6 Response Time vs.

Load Resistance



Collector-Emitter Voltage Vce (V)



Packing Quantity Specification

- 1. 1000Pcs/1Bag, 10 Bag/1Box
- 2. 4Boxes/1Carton

Label Form Specification



· PRODUCT: Part Number

· CODE NO.: Product Serial Number

· QTY: Packing Quantity

· LOT No: Lot Number

· REMARKS:Remarks

Notes

Lead Forming

1. During lead frame bending, the lead frame should be bent at a distance more than 3mm from bottom of the epoxy.

Note: Must fix lead frame and do not touch epoxy before bending to avoid Phototransistor broken.

- 2.Lead forming should be done before soldering.
- 3. Avoid stressing the Phototransistor package during leads forming. The stress to the base may damage the Phototransistor's characteristics or it may break the Phototransistor.
- 4.Cut the Phototransistor lead frame at room temperature. Cutting the lead frame at high temperatures may cause failure of the Phototransistor.
- 5. When mounting the Phototransistor onto a PCB, the PCB holes must be aligned exactly with the lead position of the Phototransistor. If the Phototransistor are mounted with stress at the leads, it causes deterioration of the epoxy resin and this will degrade the Phototransistor.

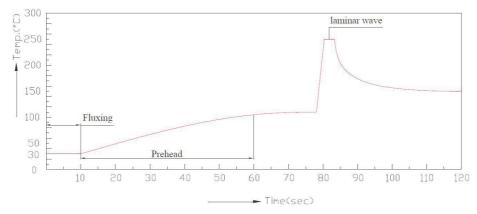


Soldering

- 1. Careful attention should be paid during soldering. When soldering, leave more than 3mm from solder joint to epoxy bulb, and soldering beyond the base of the tie bar is recommended.
- 2. Recommended soldering conditions:

Hand Soldering		DIP Soldering		
Temp. at tip of iron	300°C Max. (30W Max.)	Preheat temp.	100°C Max. (60 sec Max.)	
Soldering time	3 sec Max.	Bath temp. & time	260 Max., 5 sec Max	
	3mm Min.(From solder		3mm Min. (From solder joint	
Distance	joint to epoxy bulb)	Distance	to epoxy bulb)	

3. Recommended soldering profile



- 4. Avoiding applying any stress to the lead frame while the Phototransistor are at high temperature particularly when soldering.
- 5.Dip and hand soldering should not be done more than one time
- 6.After soldering the Phototransistor, the epoxy bulb should be protected from mechanical shock or vibration until the Phototransistor return to room temperature.
- 7.A rapid-rate process is not recommended for cooling the Phototransistor down from the peak temperature.
- 8. Although the recommended soldering conditions are specified in the above table, dip or hand soldering at the lowest possible temperature is desirable for the Phototransistor.
- 9. Wave soldering parameter must be set and maintain according to recommended temperature and dwell time in the solder wave.



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