

Features

High sensitivity

Low capacitance

Short switching time

Wide temperature range

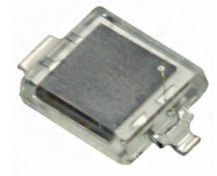
Small package

Pb free

The product itself will remain within RoHS compliant version.

Compliance with EU REACH.

Compliance Halogen Free .(Br <900 ppm ,Cl <900 ppm , Br+Cl < 1500 ppm).



Application

High speed photo detector

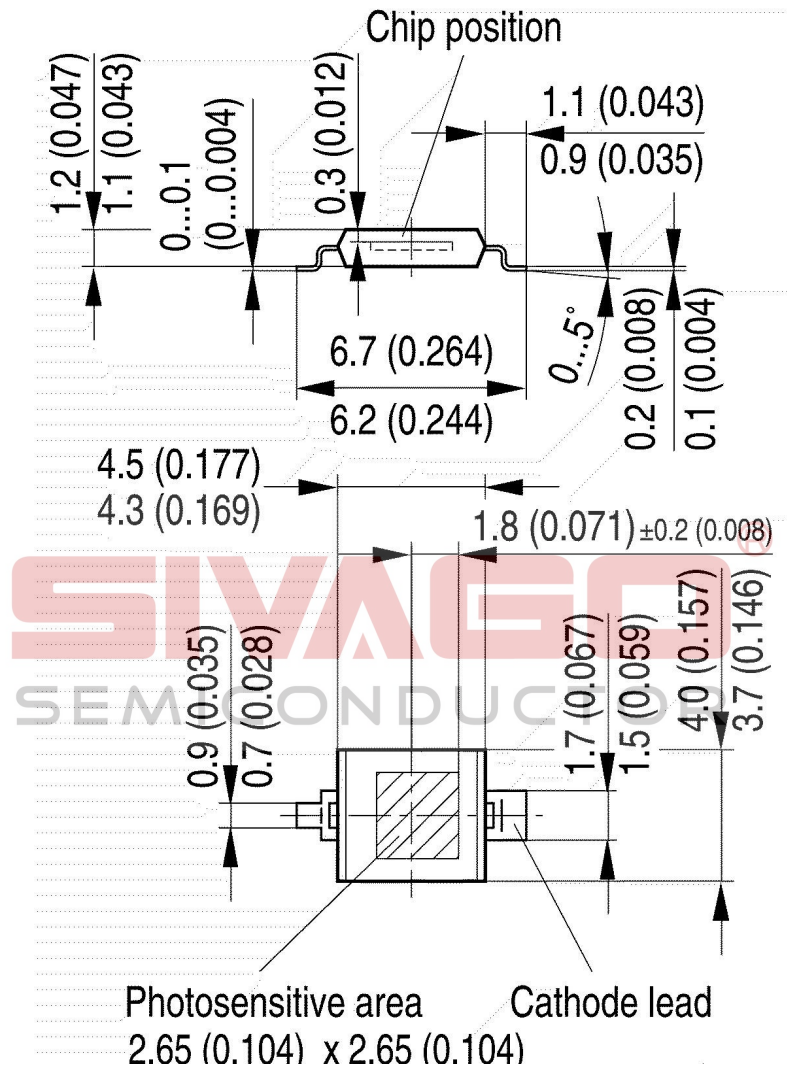
Copier

Elevator

Description

The PD700C is high sensitivity, fast switching times, low capacitance, compact size, and lack of measurable degradation make it suitable for diverse applications, such as TV and appliance

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SEMICONDUCTOR

PACKAGE DIMENSIONS

NOTES:

1. All dimensions are in millimeters (inches).
2. Tolerance is $\pm 0.25\text{mm}$ (.010") unless otherwise noted.
3. Lead spacing is measured where the leads emerge from the package.

Maximum Ratings $T_A = 25\text{ °C}$

Parameter	Symbol	Values
Operating Temperature	T_{op}	min. -40 °C
		max. 100 °C
Storage temperature	T_{stg}	min. -40 °C
		max. 100 °C
Reverse voltage	V_R	max. 32 V
Total power dissipation	P_{tot}	max. 150 mW
ESD withstand voltage acc. to ANSI/ESDA/JEDEC JS-001 (HBM, Class 2)	V_{ESD}	max. 2 kV

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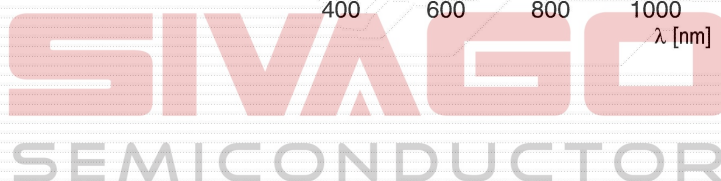
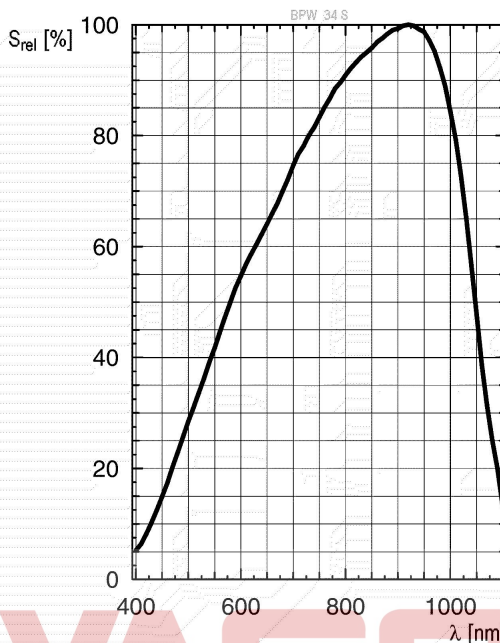
Characteristics

$T_A = 25\text{ }^\circ\text{C}$

Parameter	Symbol		Values
Wavelength of max sensitivity	$\lambda_{S\max}$	typ.	920 nm
Spectral range of sensitivity	$\lambda_{10\%}$	typ.	420 ... 1120 nm
Radiant sensitive area	A	typ.	7.02 mm ²
Dimensions of active chip area	L x W	typ.	2.65 x 2.65 mm x mm
Half angle	φ	typ.	60 °
Dark current	I_R	typ.	2 nA
$V_R = 10\text{ V}$		max.	30 nA
Spectral sensitivity of the chip	S_λ	typ.	0.62 A / W
$\lambda = 850\text{ nm}$			
Quantum yield of the chip	η	typ.	0.90 Electrons / Photon
$\lambda = 850\text{ nm}$			
Open-circuit voltage	V_O	min.	300 mV
$E_v = 1000\text{ lx}$; Std. Light A; $V_R = 0\text{ V}$		typ.	365 mV
Short-circuit current	I_{SC}	typ.	80 μA
$E_v = 1000\text{ lx}$; Std. Light A; $V_R = 0\text{ V}$			
Rise time	t_r	typ.	0.02 μs
$V_R = 5\text{ V}$; $R_L = 50\text{ }\Omega$; $\lambda = 850\text{ nm}$			
Fall time	t_f	typ.	0.02 μs
$V_R = 5\text{ V}$; $R_L = 50\text{ }\Omega$; $\lambda = 850\text{ nm}$			
Forward voltage	V_F	typ.	1.3 V
$I_F = 100\text{ mA}$; $E = 0$			
Capacitance	C_0	typ.	72 pF
$V_R = 0\text{ V}$; $f = 1\text{ MHz}$; $E = 0$			
Temperature coefficient of voltage	TC_V	typ.	-2.6 mV / K
Temperature coefficient of short-circuit current	TC_I	typ.	0.18 % / K
Std. Light A			
Noise equivalent power	NEP	typ.	0.041 pW / Hz ^{1/2}
$V_R = 10\text{ V}$; $\lambda = 850\text{ nm}$			
Detection limit	D^*	typ.	6.5e12 cm x Hz ^{1/2} / W
$V_R = 10\text{ V}$; $\lambda = 850\text{ nm}$			

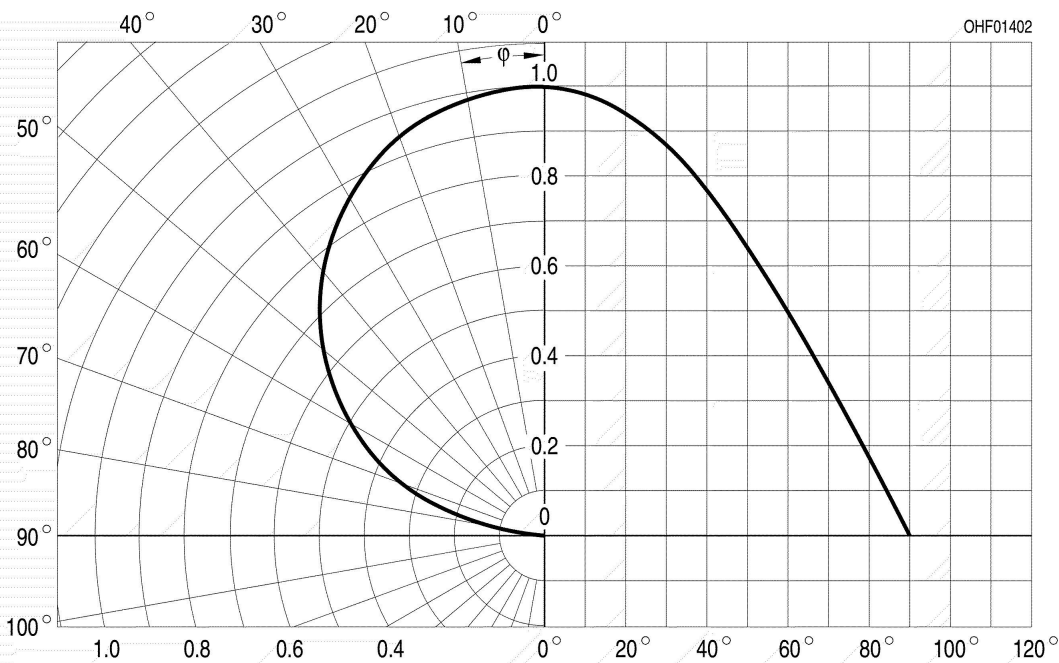
Relative Spectral Sensitivity ^{2), 3)}

$$S_{rel} = f(\lambda)$$



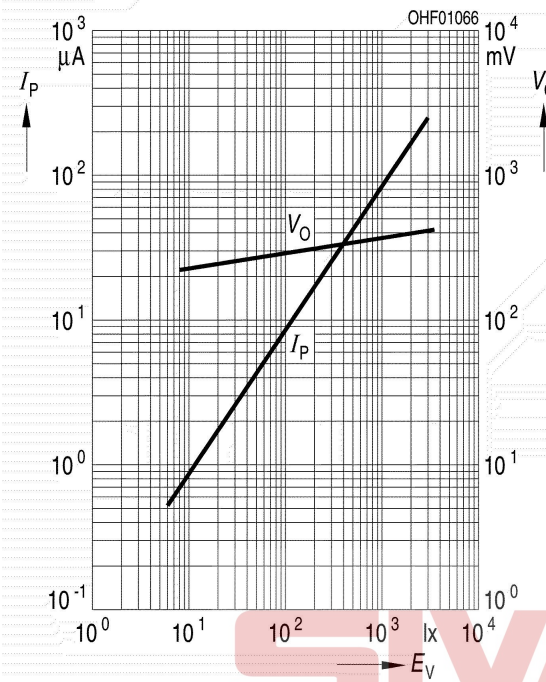
Directional Characteristics ^{2), 3)}

$$S_{rel} = f(\varphi)$$



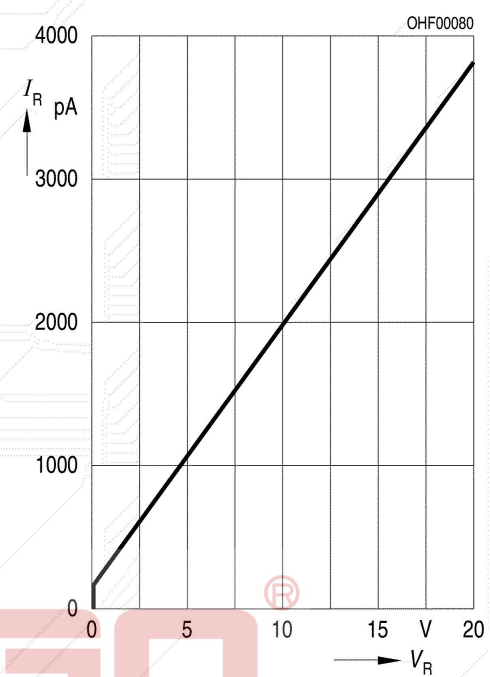
Photocurrent/Open-Circuit Voltage ^{2), 3)}

$$I_P (V_R = 5 \text{ V}) / V_O = f(E_V)$$



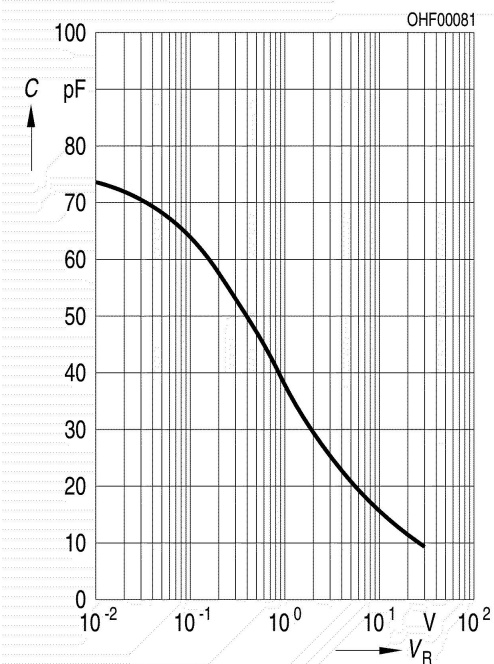
Dark Current ^{2), 3)}

$$I_R = f(V_R); E = 0$$



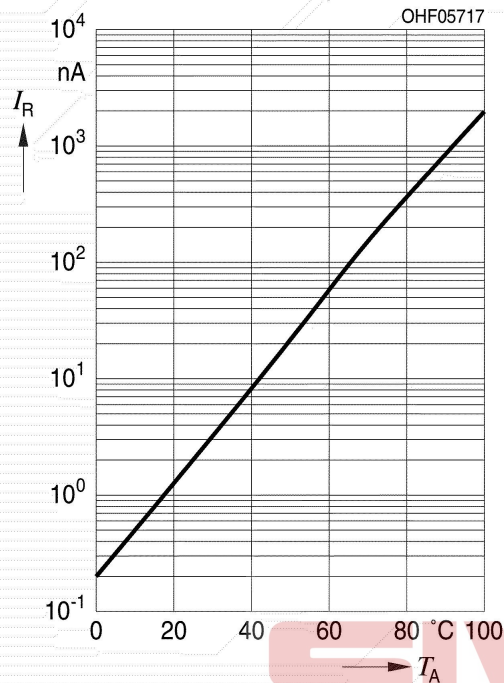
Capacitance ^{2), 3)}

$$C = f(V_R); f = 1\text{MHz}; E = 0; T_A = 25^\circ\text{C}$$



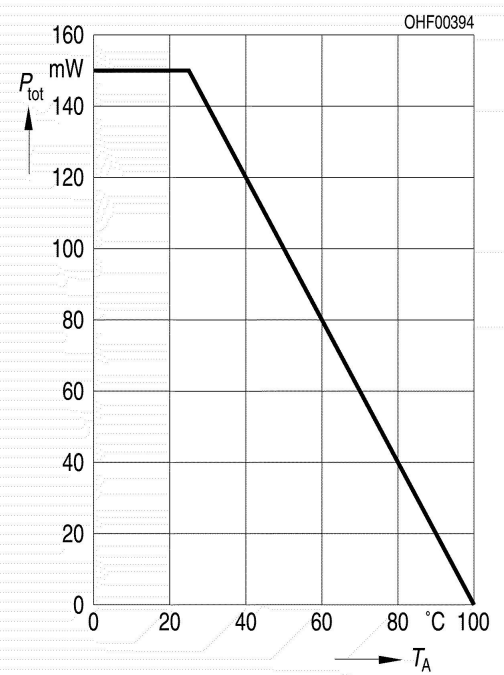
Dark Current ²⁾

$$I_R = f(T_A); E = 0; V_R = 10 \text{ V}$$

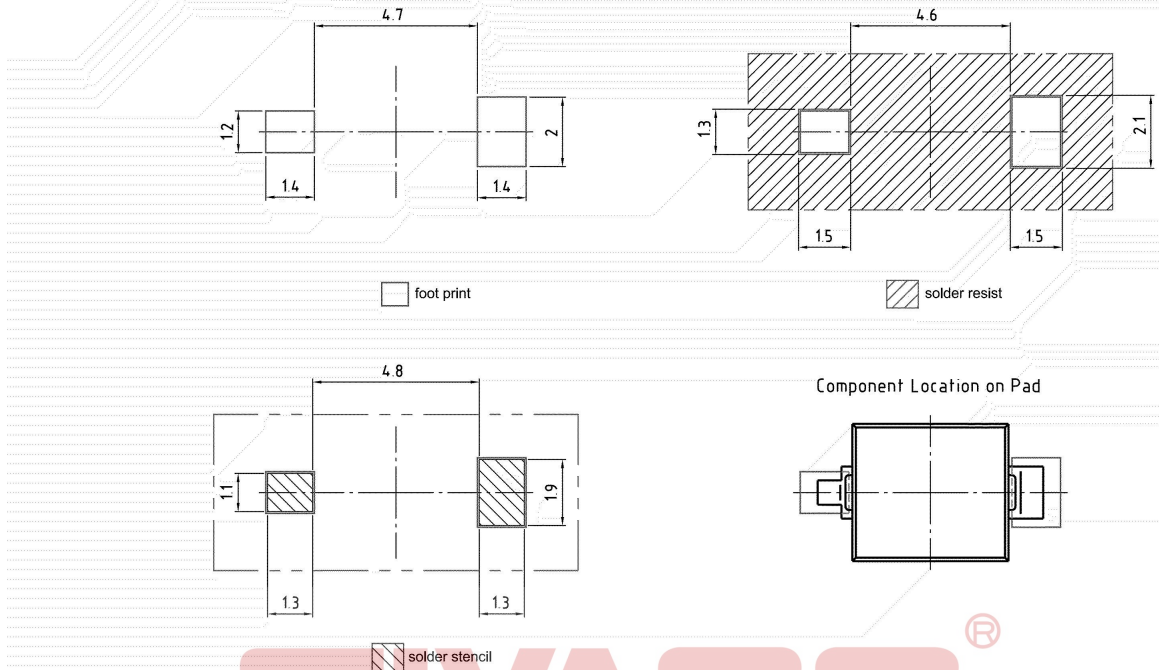


Power Consumption

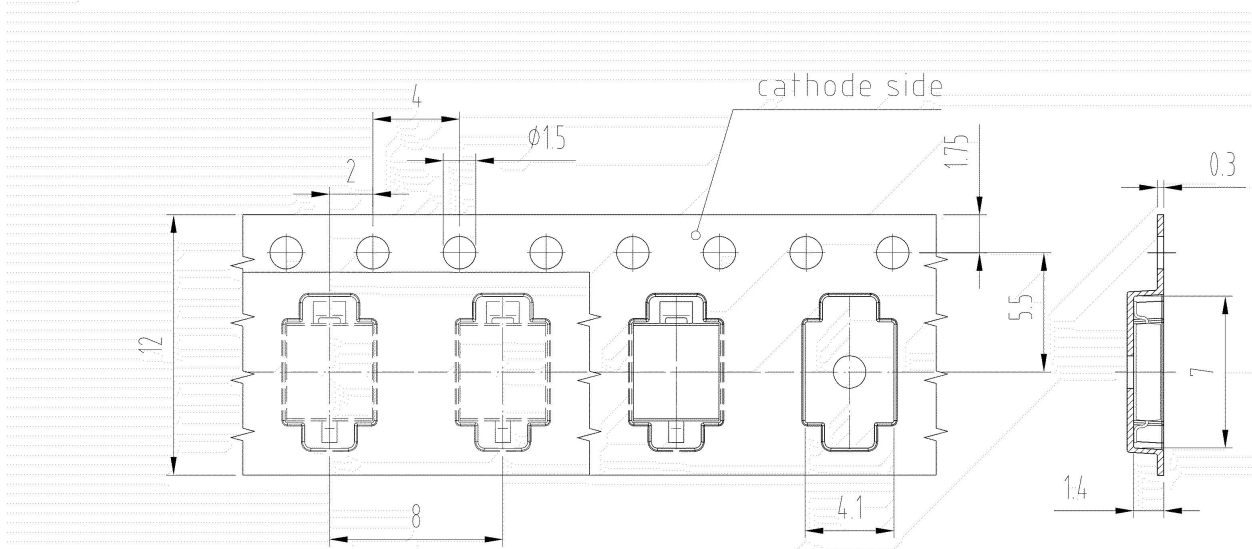
$$P_{tot} = f(T_A)$$



Recommended Solder Pad ⁴⁾



Taping ⁴⁾



Packing Quantity Specification

1. 1500Pcs/1Bag, 10 Bag/1Box
2. 4Boxes/1Carton

Label Form Specification

製品名 PRODUCT	
コードNo. CODE No.	
数量 Q'TY	
ロットNo. LOT No.	
備考 REMARKS	
	

- PRODUCT: Part Number
- CODE NO.: Product Serial Number
- QTY: Packing Quantity
- LOT No: Lot Number
- REMARKS:Remarks

Notes**Lead Forming**

1. During lead frame bending, the lead frame should be bent at a distance more than 3mm from bottom of the epoxy.

Note: Must fix lead frame and do not touch epoxy before bending to avoid Photodiode broken.

2. Lead forming should be done before soldering.

3. Avoid stressing the Photodiode package during leads forming. The stress to the base may damage the Photodiode's characteristics or it may break the Photodiode.

4. Cut the Photodiode lead frame at room temperature. Cutting the lead frame at high temperatures may cause failure of the Photodiode.

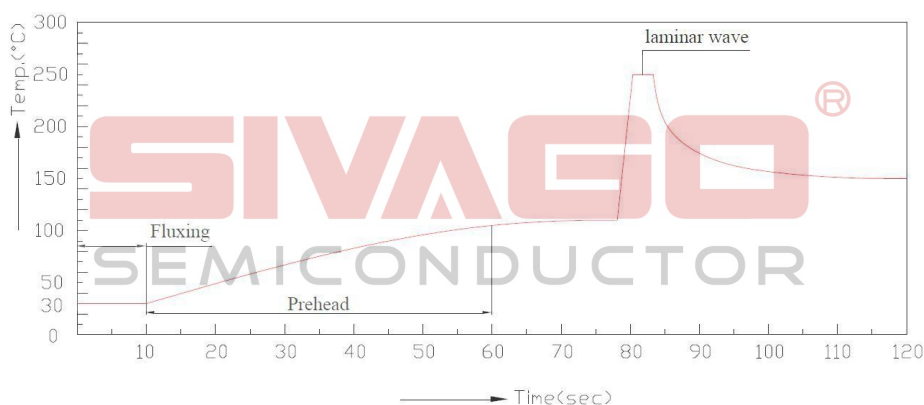
5. When mounting the Photodiode onto a PCB, the PCB holes must be aligned exactly with the lead position of the Photodiode. If the Photodiode are mounted with stress at the leads, it causes deterioration of the epoxy resin and this will degrade the Photodiode.

Soldering

- Careful attention should be paid during soldering. When soldering, leave more than 3mm from solder joint to epoxy bulb, and soldering beyond the base of the tie bar is recommended.
- Recommended soldering conditions:

Hand Soldering		DIP Soldering	
Temp. at tip of iron	300°C Max. (30W Max.)	Preheat temp.	100°C Max. (60 sec Max.)
Soldering time	3 sec Max.	Bath temp. & time	260 Max., 5 sec Max
Distance	3mm Min.(From solder joint to epoxy bulb)	Distance	3mm Min. (From solder joint to epoxy bulb)

3. Recommended soldering profile



- Avoiding applying any stress to the lead frame while the Photodiode are at high temperature particularly when soldering.
- Dip and hand soldering should not be done more than one time
- After soldering the Photodiode, the epoxy bulb should be protected from mechanical shock or vibration until the Photodiode return to room temperature.
- A rapid-rate process is not recommended for cooling the Photodiode down from the peak temperature.
- Although the recommended soldering conditions are specified in the above table, dip or hand soldering at the lowest possible temperature is desirable for the Photodiode.
- Wave soldering parameter must be set and maintain according to recommended temperature and dwell time in the solder wave.

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