OPTO INTERRUPTER

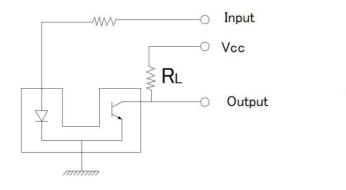


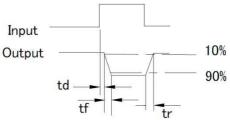
Features

Non-contact switching.
For direct PC board or dual-in-line socket mounting.
Fast switching speed.
Application
Scanner
Edge,Position Detections
FAX machine
Counter
Counter
Bescription
The AEDS9100 series consist of Gallium Arsenide infrared emitting diode and a NPN sillic

The AEDS9100 series consist of Gallium Arsenide infrared emitting diode and a NPN sillicon phototransistor mounted in a black plastic housing. Phototransistor switching takes place whenever an opaque object passes through the slot. These series are designed for direct soldering into PC board or mounting in standard dual-in-line socket.

Switching time measurement circuit

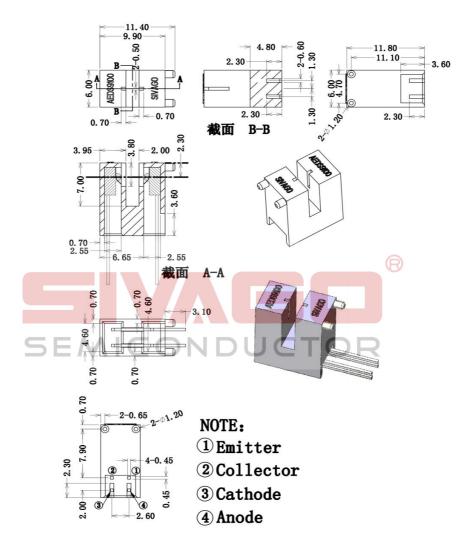




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PACKAGE DIMENSIONS



NOTES:

- 1. All dimensions are in millimeters (inches).
- 2. Tolerance is ±0.25mm(.010") unless otherwise noted.
- 3. Lead spacing is measured where the leads emerge from the package.



ABSOLUTE MAXIMUM RATINGS AT TA =25°C

PARAMETER	MAXIMUM RATING	UNIT
IR Diode Continuous Forward Current	50	mA
IR Diode Reverse Voltage	5	V
Transistor Collector Currant	20	mA
Transistor Power Dissipation	100	mW
IR Diode Peak Power Currant	3	Α
(Pulse Wide = 1μ S, 300 pps)		
Diode Power Dissipation	175	mW
Phototransistor Collector-Emitter Voltage	30	V
Phototransistor Emitter-Collector Voltage	5	V
Operating Temperature Range	-40°C to +85	5°C
Storage Temperature Range	UCTO 50°C to +100)°C

AEDS9100



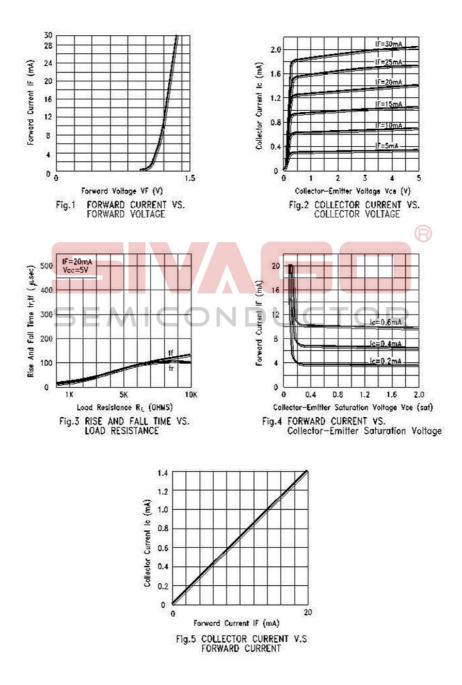
ELECTRICAL OPTICAL CHARACTERISTICS AT TA=25°C

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION
INPUT LED						
Forward Voltage Reverse Current	VF		1.2	1.35	V	$V I_F = 20 m A$
Reverse Current	IR			100	μ	A VR=5V
OUTPUT PHOTOTRANSISTOR						
Collector-Emitter Breakdown Voltage	V(BR)ceo	30			V	V IC=1mA
Emitter-Collector Breakdown Voltage	V(BR)ceo	5			V	V IE=0.1mA
Collector-Emitter Dark Current	Iceo			100	nA 🕞	VCE=10V
COUPLER						
Collector-Emitter Saturation Voltage	VCE(SAT)		UC		VR	IC=0.2mA IF=20mA
Current Transfer Ratio	Ic(on)	0.8			mA	VCE=5V IF=20mA



TYPICAL ELECTRICAL / OPTICAL CHARACTERISTICS CURVES

(25°C Ambient Temperature Unless Otherwise Noted)



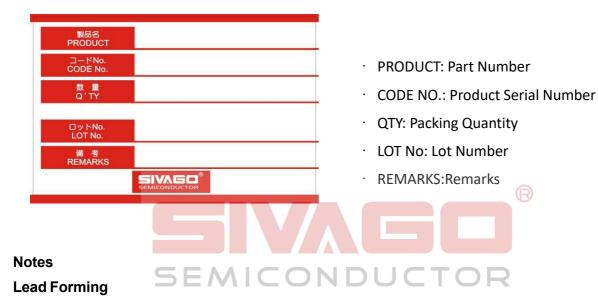
OPTO INTERRUPTER



Packing Quantity Specification

- 1. 200Pcs/1Bag,10 Bag/1Box
- 2. 4Boxes/1Carton

Label Form Specification



1. During lead frame bending, the lead frame should be bent at a distance more than 3mm from bottom of the epoxy.

Note: Must fix lead frame and do not touch epoxy before bending to avoid Photo Interrupter broken.

2. Lead forming should be done before soldering.

3. Avoid stressing the Photo Interrupter package during leads forming. The stress to the base may damage the characteristics of Photo Interrupter, or it may break the Photo Interrupter.

4. Cut the Photo Interrupter lead frame at room temperature. Cutting the lead frame at high temperatures may cause failure of the Photo Interrupter.

5. When mounting the Photo Interrupter onto a PCB, the PCB holes must be aligned exactly with the lead position of the Photo Interrupter. If the Photo Interrupter are mounted with stress at The leads, it causes deterioration of the epoxy resin and this will degrade the Photo Interrupter.

OPTO INTERRUPTER



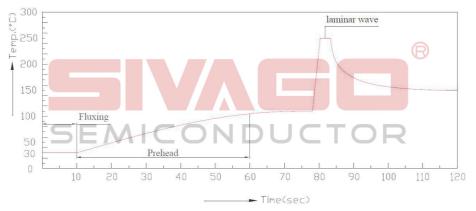
Soldering

1. Careful attention should be paid during soldering. When soldering, leave more than 3mm from solder joint to epoxy bulb, and soldering beyond the base of the tie bar is recommended.

2. Recommended soldering conditions:

Hand Soldering		DIP Soldering		
Temp. at tip of iron	300°C Max. (30W Max.)	Preheat temp.	100°C Max. (60 sec Max.)	
Soldering time	3 sec Max.	Bath temp. & time	260 Max., 5 sec Max	
	3mm Min.(From solder		3mm Min. (From solder joint	
Distance	joint to epoxy bulb)	Distance	to epoxy bulb)	

3. Recommended soldering profile



4. Avoiding applying any stress to the lead frame while the Photo Interrupter are at high temperature particularly when soldering.

5. Dip and hand soldering should not be done more than one time

6. After soldering the Photo Interrupter, the epoxy bulb should be protected from mechanical shock or vibration until the Photo Interrupter return to room temperature.

7. A rapid-rate process is not recommended for cooling the Photo Interrupter down from the peak temperature.

8. Although the recommended soldering conditions are specified in the above table, dip or hand soldering at the lowest possible temperature is desirable for the Photo Interrupter.

9. Wave soldering parameter must be set and maintain according to recommended temperature and dwell time in the solder wave.



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